

BOSE INSTITUTE KOLKATA

Tender No.	CAPSS/15/126(ALICE-SD)/13-14
Tender Date	05/12/2013
Tender Type	Limited

Tender Title	FPGA Evaluation Board
Specification	<p><u>FPGA:</u></p> <ul style="list-style-type: none"> • ROHS Evaluation kit including the XC7VX485T-2FFG1761 FPGA Chip <p><u>Configuration</u></p> <ul style="list-style-type: none"> • Onboard JTAG configuration circuitry to enable configuration over USB • JTAG header provided for use with Platform Cable USB II • 128MB (1024Mb) Linear BPI Flash for PCIe® Configuration • 16MB (128Mb) Quad SPI Flash <p><u>Memory</u></p> <ul style="list-style-type: none"> • 1GB DDR3 SODIMM 800MHz / 1600Mbps • 128MB (1024Mb) Linear BPI Flash for PCIe Configuration • SD Card Slot <p><u>Communication & Networking</u></p> <ul style="list-style-type: none"> • Gigabit Ethernet GMII, RGMII and SGMII • SFP+ transceiver connector • GTX port (TX, RX) with four SMA connectors • UART To USB Bridge • PCI Express x8 gen2 Edge Connector (lay out for Gen3) <p><u>Display</u></p> <ul style="list-style-type: none"> • HDMI Video OUT • 2 x16 LCD display • 8X LEDs <p><u>Expansion Connectors</u></p> <ul style="list-style-type: none"> • FMC Connectors

	<p><u>Clocking</u></p> <ul style="list-style-type: none"> • Fixed Oscillator with differential 200MHz output • Programmable Oscillator with 156.250 MHz as the default output • Differential SMA clock input • Differential SMA GTX reference clock input • Jitter attenuated clock <p><u>Control & I/O</u></p> <ul style="list-style-type: none"> • 5X Push Buttons • 8X DIP Switches <p><u>Power</u></p> <ul style="list-style-type: none"> • 12V wall adapter or ATX • Voltage and Current measurement capability <p><u>Debug & Analog Input</u></p> <ul style="list-style-type: none"> • 8 GPIO Header, 9 pin removable LCD • Analog Mixed Signal (AMS) Port
Quantity	TWO

Last Date & Time for submission	26/12/2013 upto 2.00 pm
Date & Time for opening bids	26/12/2013 at 3.00 p.m.
Submission of Tender (address)	CAPSS, Block EN 80, Salt Lake, Kol - 91
Venue of bid opening	CAPSS, Block EN 80, Salt Lake, Kol - 91
For any query the interested bidders may contact (Dept./Section/Div./Unit)	033 2569 3109/3123

General Terms & Conditions

Warranty	12 months from the date of satisfactory installation
Payment terms	Payment will be made after complete delivery of the material in good condition and satisfactory installation
Delivery schedule	Within 30 days from the date of order and if any defect of the supplied item is found it should be replaced immediately from your side
Bid security (earnest money deposit) if applicable	NA

Submission of Performance Bank Guarantee (PBG), if applicable	NA
Any other information (if applicable)	In case of an Indian representative / authorized seller quoting for a product from foreign principal, an authorization certificate from the principal is mandatory for tender qualification.

Name of the instrument and submission of tender should be mentioned on the envelop positively

Director, Bose Institute reserves the right to accept or reject any or all tenders either in part or in full. The reasons for rejecting the tender of a prospective bidder will be disclosed only when enquiries are made.

Registrar, B.I.