

(भारत रारकार के विज्ञान और प्रौद्योगिकी मंत्रालय द्वारा वित्तपोषित सोसाइटी)

(A Society for Scientific Research financed by the Ministry of Science & Technology, Govt. of India)

ADVT. NO.: S/CAPSS/26/2018-19

A Walk-in-Interview will be held on 12th October, 2018 at 11.00 a.m. in the CAPSS Campus of Bose Institute, Block – EN – 80, Sector - V, Salt Lake, Kolkata – 700 091 for one position of <u>Technical Assistant</u> (<u>Electronics</u>) in the **DST** sponsored project entitled, "CBM MUCH" at FAIR under Prof. Sanjay K. Ghosh, Principal Investigator and Dr. Supriya Das, Co-Investigator, Department of Physics (CAPSS). The project is tenable upto 31.07.2022. Details of qualification, pay, age etc. are as follows:

<u>Essential Qualification</u>: Candidates must have diploma in Electrical / Electronics and Instrumentation / Electrical and Electronics Engineering.

<u>Desirable Experience</u>:

- (a) Candidates should have experience in computer programming.
- (b) Candidates must have good knowledge in digital electronics.
- (c) Candidates should have knowledge of PCB design.
- (d) Working experience with microcontroller

Job Profile: Selected candidate will work on development and fabrication of electronics for detectors. He/She will be required to work on the development of the high-energy physics laboratory, procurement of materials etc.

Pay: Rs. 12,500/- (consolidated).

Age Limit: Below 30 years (relaxable in case of SC/ST/OBC/WOMEN candidates only as per rule).

Interested and eligible candidates are requested to appear before the Selection Committee with typed application addressed to the Acting Registrar, Bose Institute, P-1/12, C.I.T. Scheme VII-M, Kolkata - 700 054 along with Bio-data giving details of qualifications with attested copies of marksheets, certificates from Secondary onwards and other testimonials etc.. The candidates should also bring the originals of the same at the time of Interview. No TA/DA is admissible for appearing at the interview.

ACTING REGISTRAR